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...Frequency Multiplier—Phase Locked Loop Vctl

$K_vco V_{ctl} + f_0 = f_{out}/N = f_{ref}$ At Steady State N

$\cos(2\pi f_{ref} t + \phi_{ref}) \cos(2\pi f_{out} t) \cos(2\pi f_{out}/N T +$

$\phi_{out}/N) V_{ctl} = K_{pd}(\phi_{ref} - \phi_{out}/N)$ Phase Detector Use A

Phase Detector To Generate The Control Voltage

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