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Lab 1 - ALU (Arithmetic/Logical Unit) CSE372 Hardware ...Prof. Milo Martin Unit 1: Synthesizable Verilog SE372 (Martin): Synthesizable Verilog 2 Lab 1 - ALU (Arithmetic/Logical Unit) ¥Task: Design An ALU For A P37X CPU ¥Ten Operations: ¥Addition, Subtraction ¥Multiplication ¥And, Or, Not, Xor ¥Shift Left, Logical Shift Right, Arithmetic Shi Mar 4th, 2024  
Parameterized Posit Arithmetic Hardware Generator Accuracy Than Their IEEE 754 Compliant floating Point Unit Counterparts. The Major Contributions In This Paper Are As Follows: We Present The first Complete Parameterized, Area-efficient And Energy-efficient PAU Generator That Can Generate Posit Adders And Posit Multipliers With Different Sizes And ... Feb 6th, 2024  
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