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Elements Where The Logic State 1 (TRUE) Is Converted
To A Logic State 0 (FALSE) Or Vice Versa [IEC
60617-12, IEC 61082-2] 3.20 Logic Inversion Condition
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2 ...Project 2 - Driving A 7-Segment Display: In This
Project We Are Going To Connect A 7-segment Display
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To 9. Project 3 - Electronic Dice: This Project Uses A
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