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Timer NE/SA/SE555/SE555C August 31, 1994 346 853-0036 13721 DESCRIPTION The 555 Monolithic Timing Circuit Is A Highly Stable Controller Capable Of Producing Accurate Time Delays, Or Oscillation. In The Time Delay Mode Of Operation, The Time Is Precisely Controlled By One External Resistor And Capacitor. For A Stable Operation As An Oscillator, The Apr 5th, 2024

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