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- Is Fast, With Speeds From 10 Mbps To 10 Gbps
- Requires Little Wiring, Typically A Single Cable Connecting To Each Device
- Has Lower Cost Compared To MAN's Or WAN's

Local Area Network (LAN)  
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ROM Chip ROM Can Only Read, The Data Bus Can Only Be In An Output Mode. For The Same Size Chip, It Is Possible To Have More Bits Of ROM Than Of RAM, Because

The Internal Binary Cells In ROM Occupy Less Space Th Apr 8th, 2024

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Principles On Which OSI Model Was Designed: A Layer Should Be Created Where Different Level Of Abstraction Is Needed. Each Layer Should Perform A Well Defined Function. The Function Of Each Layer Should Be Chosen According To The Internationally Standardized Protocols. The Number Of Layers Should Be Large Enough That Distinc Apr 4th, 2024

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(2) To Determine The Proper Number Of Sig. Figs When Multiplying Or Dividing The Measurement With The Least Number Of Sig. Figs Is Boss G. If All This Is Confusing, Use The (A)tantic - (P)acific Rule. If The Decimal Point Is (A)bsent - Start Counting Significant Figures From The Atlantic Feb 17th, 2024

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Been Made To Envisage The Operation Of A Digital Computer In Step By Step



Manner. 1.1 INTRODUCTION Microprocessor Is A Digital Device On A Chip Which Can Fetch Instructions From A Mar 17th, 2024

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