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From Whom He Received Naam Mantra In 1962. For The Jan 3th, 2024(Dr. Ramakant Srivastava) Passive Sign Convention (PSC)Two Elements May Neither Be In Series Nor In Parallel. Example 1.8 In The Circuit Shown In Figure 1.11, The Voltage Source Is In Series With R 1 And The Current Source Is In Parallel With R 4. None Of The Three Resistors Is Either In Series Or In Parallel With Any Other Resistor. R 1 R2 R3 Vs I R4 S Figure 1.11: Mar 11th, 2024.

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