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(1) International Technology Roadmap For Semiconductors (ITRS) 2001 Edition For CMOS Devices Table 1 Shows The 2001 Version Of The ITRS For CMOS

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## **Technologies**

This Paper Tries To Present A Comparative Picture Of The Different Additive Manufacturing Technologies.

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Project Work Was Focused On Four Submarine Alignment And Inspection Applications; Shafts, Torpedo Tubes, Sails, And Dive Planes. It Was Understood That Many More Applications Would Ult  
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### **Developing Polymer Additive Manufacturing Technologies**

Developing Polymer Additive Manufacturing Technologies . Monday, December 17, 2018 | 11 A.m. 2164 Martin Hall, DeWALT Seminar Room. ABSTRACT. The Army Research Laboratory Is Developing Polymer Additive Manufacturing (AM) Technologies To Make Replacement Parts For Army Weapons, Pl Jan 5th, 2024

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