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Engineering Time And Effort. Thanks To My Families And Friends For Their Constant Support And Encouragement. Special Thanks To Mom And Dad. Without Them Calling Me And Nagging At Me On A Jan 1th, 2024

Trace-driven simulation Of Multithreaded Applications In Gem5`Interconnect - Bus Parameters,... `Component Implementation: `Execution Behavior `Communication Protocols Core 0 Core 1 | D | D L2 Memory Parameters 21 `Specification Information: `CPU - Number Of Cores, Core Types, Etc. `Memory - Cache, Coherency, Main Memory, Etc. `Interconnect - Bus Parameters,... `Component Implementation: Apr 10th, 2024.

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