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### Betty Schulte, RHIT, CCS, CCS-P P.O. Box 27 Sabinal, Texas ...

Betty Schulte, RHIT, CCS, CCS-P P.O. Box 27 Sabinal, Texas 78881 830-423-4416
Betty.schulte@sbcglobal.net EMPLOYMENT
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Discrete-time Analog Output Y[n]=v(nT) And Can Be Analyzed As A Purely Discrete-time System. In The Following, DEM Is first Explained In This Discrete-time Context. Then, The Results Are Extended To The Continuous-time Case. Consider A Three-level Discrete-time DAC With Ideal Output V Mar 9th, 2024

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Full-scale Output Of 4.095V In A  $^{\prime}$  2 Gain Configuration. It Operates From A Single 4.5V To 5.5V Supply Dissipating Only 5.5mW (I CC = 1.1mA Typ). The LTC1458L Has An Onboard 1.22V Reference And A Full-scale Output Of 2.5V In A  $^{\prime}$  2 Gain Configuration. It Operate Jan 16th, 2024

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The Design Implementation Of The DAC IC Is Dependent On The Performance Desired From The DAC. For Example, One May Want A High Speed, High Resolution, High Accuracy DAC That May Trade Off By Pulling A Large Supply Current, And Thus Have High Power Dissipation. Or One May Want A Low Power DAC Mar 4th, 2024

#### 10-Bit Voltage-Output DACs In 8-Pin µMAX

The MAX5354/MAX5355 Combine A Low-power, Voltage-output, 10-bit Digital-to-analog Converter (DAC) And A Precision Output Amplifier In An 8-pin µMAX Or DIP Pack-age. The MAX5354 Operates From A Single +5V Supply, And The MAX5355 Operates From A Single +3.3V Sup Mar 7th, 2024

#### MT-015: Basic DAC Architectures II: Binary DACs

MT-015. While Some Types Of Multiplying DACs Will Work Only With References Of One Polarity (two Quadrant) Others Handle Bipolar (positive Or Negative) References, And Can Work With An Ac Signal As A Reference As Well. A Bipolar DAC T Jan 14th, 2024

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