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MGP096 Gp (dB) PL (W) PEP Fig.9 Power Gain As A Function Of Load Power; Typical
Values. Class-AB Operation; VDS = 50 V; IDQ = 0.7 A; RGS = 5Ω; F1 = 28.000 MHz;
F2 = 28.001 MHz. Handbook, Halfpage 0 60 40 20 0 100 200 Feb 12th, 2024

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0 50 100 150 200 0 100 MGP206 RDSon (mΩ) Tj (°C) Fig.7 Input And Output
Capacitance As Functions Of Drain-source Voltage; Typical Values Per Section. VGS
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Directly To The Ground. Therefore, CMOS Circuits Consume Much Less Power Than Other Types Of Circuits. Figure 6-7b Illustrates How NFET And PFET Can Be Fabricated On The Same Chip. Portions Of The P-type Substrate Are Converted Into N-type Wells By Donor Implantation And Diffusion. Conta Jan 24th, 2024

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