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0 50 100 150 200 0 100 MGP206 RDSon (mΩ) Tj (°C) Fig.7 Input And Output Capacitance As Functions Of Drain-source Voltage; Typical Values Per Section. VGS = 0; F = 1 MHz. Handbook, Halfpage 0 Mar 2th, 2024

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### **MOS Transistor - Chenming Hu**

Directly To The Ground. Therefore, CMOS Circuits Consume Much Less Power Than Other Types Of Circuits. Figure 6-7b Illustrates How NFET And PFET Can Be Fabri-cated On The Same Chip. Portions Of The P-type Substrate Are Converted Into N-type Wells By Donor Implantation And Diffusion. Conta Mar 23th, 2024

### **Lecture 12: MOS Transistor Models**

C VV CV V Qy -+ - ≈- Source End Drain End ( ) ( ) 2 Ox GS T Ox GS SD T N C VV CVVV Qy Jan 17th, 2024

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